

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	255	(sram "static random access memory") with initializ\$5	USPAT	OR	OFF	2006/02/08 17:12
S2	651	initialization adj code\$1	USPAT	OR	OFF	2005/03/16 14:15
S3	3	(sram "static random access memory") with (initialization adj code\$1)	USPAT	OR	OFF	2003/10/28 11:59
S4	7	(sram "static random access memory") with (initialization adj memory)	USPAT	OR	OFF	2003/10/28 12:08
S5	42	instant adj (ram sram)	USPAT	OR	OFF	2003/10/28 12:19
S6	5	("6442625" "6438687" "6330622" "6161051" "6154788").pn.	USPAT	OR	OFF	2005/08/26 16:41
S7	2272	rom with ram with (start\$3 boot\$3 initializ\$5)	USPAT	OR	OFF	2003/10/28 13:11
S8	72	rom with sram with (start\$3 boot\$3 initializ\$5)	USPAT	OR	OFF	2003/10/28 13:11
S9	218	dram near3 initializ\$5	USPAT	OR	OFF	2003/10/31 12:05
S10	0	(dram near3 initializ\$5) with bios	USPAT	OR	OFF	2003/10/31 12:05
S11	44	(dram near3 initializ\$5) same bios	USPAT	OR	OFF	2003/10/31 12:07
S12	174	(dram near3 initializ\$5) not ((dram near3 initializ\$5) same bios)	USPAT	OR	OFF	2003/10/31 12:15
S13	592	ROM near3 (extend\$3 extension)	USPAT	OR	OFF	2003/10/31 12:17
S14	1	(ROM near3 (extend\$3 extension)) with sram	USPAT	OR	OFF	2003/10/31 12:20
S15	21	merg\$3 near2 rom	USPAT	OR	OFF	2003/10/31 13:05
S16	593	(sram "static random access memory") with (boot\$3 start\$3)	USPAT	OR	OFF	2004/03/20 21:13
S17	42	((sram "static random access memory") with (boot\$3 start\$3)) and "713"/\$.cccls.	USPAT	OR	OFF	2004/03/20 21:27
S18	1	"6321278".pn.	USPAT	OR	OFF	2005/08/26 17:30
S19	35	(sram "static random access memory") with initializ\$5 with (dram "dynamic random access memory")	USPAT	OR	OFF	2005/03/11 16:38
S20	41	(sram "static random access memory") with initializ\$5 with (dram "dynamic random access memory")	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/11 16:50
S21	7	(sram "static random access memory") with (initializ\$5 near2 (dram "dynamic random access memory"))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 14:08

S22	1	(dram "dynamic random access memory") adj initializ\$5 adj (code\$1 program\$1)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/11 16:58
S23	46	(dram "dynamic random access memory") adj initializ\$5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/11 16:58
S24	24543	(sram "static random access memory")	USPAT	OR	OFF	2005/03/16 13:59
S25	372	(sram "static random access memory") near2 addition\$3	USPAT	OR	OFF	2005/03/16 14:00
S26	0	(sram "static random access memory") near2 addition\$3 near5 (code\$1 instruction\$1)	USPAT	OR	OFF	2005/03/16 14:00
S27	5	(sram "static random access memory") near2 addition\$3 with (code\$1 instruction\$1)	USPAT	OR	OFF	2005/03/16 14:03
S28	235	(sram "static random access memory") near3 execut\$4	USPAT	OR	OFF	2005/03/16 14:04
S29	3	(sram "static random access memory") near3 execut\$4 with (start\$3 boot\$3)	USPAT	OR	OFF	2005/03/16 14:06
S30	8	(sram "static random access memory") near3 execut\$4 with (start\$3 boot\$3)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 14:06
S31	41	(sram "static random access memory") with (initializ\$5 with (dram "dynamic random access memory"))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 14:14
S32	84866	dram "dynamic random access memory" "dynamic ram"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 14:15
S33	866	initialization adj code\$1.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 14:15
S34	1	S32 near2 S33	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 14:16
S35	2	S32 near5 S33	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/16 14:16
S36	67	(sram "static random access memory") with before with (startup power\$3)	USPAT	OR	OFF	2005/08/26 16:42

S37	0	(sram "static random access memory") with before with startup	USPAT	OR	OFF	2005/08/26 15:36
S38	0	(sram "static random access memory") with before with startup	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/26 16:08
S39	2414	(sram "static random access memory") near3 power\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/26 16:00
S40	119	(sram "static random access memory") near3 power\$3 near5 maintain\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/26 16:01
S41	338	(sram "static random access memory") with (startup boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/26 16:09
S42	147	S41 and g06f\$.ipc.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/26 16:09
S43	1	"5805882".pn.	USPAT	OR	OFF	2005/08/26 16:41
S44	1	"5388267".PN.	USPAT; USOCR	OR	OFF	2005/08/26 16:41
S45	5	(sram "static random access memory") near3 (contain\$3 stor\$3) near2 boot	USPAT	OR	OFF	2005/08/26 17:33
S46	4	(sram "static random access memory") near3 (contain\$3 stor\$3) near2 bios	USPAT	OR	OFF	2005/08/26 17:16
S47	425	flash near3 (contain\$3 stor\$3) near2 bios	USPAT	OR	OFF	2005/08/26 17:17
S48	9	flash near3 (contain\$3 stor\$3) near2 bios with cop\$3	USPAT	OR	OFF	2005/08/26 17:17
S49	1	"5835952".pn.	USPAT	OR	OFF	2005/08/26 17:30
S50	1412	(sram "static random access memory") near3 power\$2	USPAT	OR	OFF	2005/08/26 17:33
S51	8	(sram "static random access memory") near3 power\$2 near3 "always"	USPAT	OR	OFF	2005/08/26 17:33
S52	914	(sram "static random access memory") with (persisten\$2 maintain\$3)	USPAT	OR	OFF	2005/08/29 15:44

S53	164	(sram "static random access memory") with (persisten\$2 maintain\$3) near3 power	USPAT	OR	OFF	2005/08/29 17:10
S54	0	S53 same (bios boot)	USPAT	OR	OFF	2005/08/29 15:44
S55	43	(thomas near2 lee).xa. and international.as.	USPAT	OR	OFF	2005/08/29 17:10
S56	193	(address near2 comparator).ab.	USPAT	OR	OFF	2005/08/29 17:11
S57	0	S55 and S56	USPAT	OR	OFF	2005/08/29 17:11
S58	14	(address near2 comparator).ti.	USPAT	OR	OFF	2005/08/29 17:11
S59	1	S55 and S58	USPAT	OR	OFF	2005/08/29 17:11
S60	78	(sram near5 boot)	USPAT	OR	OFF	2005/09/02 19:45
S61	168	(sram with boot\$3)	USPAT	OR	OFF	2005/09/02 19:46
S62	66	(sram with boot\$3) and g06f\$.ipc.	USPAT	OR	OFF	2005/09/02 19:45
S63	326	address\$2 near3 overlay\$3	USPAT	OR	OFF	2006/02/08 17:27
S64	98	address\$2 near3 overlay\$3 with memory	USPAT	OR	OFF	2006/02/08 17:33
S65	3	(address\$2 near3 overlay\$3 with memory) same (boot initializ\$6)	USPAT	OR	OFF	2006/02/08 17:28
S66	13	address\$2 near3 overlay\$3 with memory and "713"/\$.ccls.	USPAT	OR	OFF	2006/02/08 17:30
S67	0	address\$2 near3 overlay\$3 with s dram	USPAT	OR	OFF	2006/02/08 17:34
S68	0	address\$2 near3 overlay\$3 same s dram	USPAT	OR	OFF	2006/02/08 17:33
S69	1	address\$2 near3 overlay\$3 with sram	USPAT	OR	OFF	2006/02/08 17:34